

TECH TIP

REVIEWING & EXPORTING PROJECTS USING A TIMESCALED LOGIC DIAGRAM ORACLE PRIMAVERA P6

By installing Primavera V7 Service pack 3, users can export projects to a Time Scaled Logic Diagram. Information for downloading and installing Service Pack 3 can referenced on My Oracle Support by following the provided <u>link</u>. Access to the Service pack update will require a valid and current My Oracle Support account. Information for registering your Primavera License for My Oracle Support can be found by following the provided <u>link</u>.

Once Service Pack 3 is installed, open the project you wish to export to a Timescaled Logic Diagram. Once the project is open, browse to Tools and select Timescaled Logic Diagram.



These tech tips are offered free of charge in the spirit of sharing knowledge with others. They do not include technical support, should you have a problem with them. We are always interested in how they can be improved, please do not hesitate to email us your comments. These tips have been thoroughly tested by our consultants in a variety of environments. Please read these tech tips thoroughly and be sure that you understand them before trying to use them. We can not be responsible for issues that develop because of the configuration of your hardware, technical environment or application of the tech memos. If you are not sure, then we urge you to contact Oracle technical support or your local support consultant for assistance.

Doing so will launch a Time Scaled Logic Dialog Window.

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Select the question mark for additional information regarding how project data is exported and organized in the Timescaled Logic Diagram.





Different Templates may be selected from within the Timescaled Logic Diagram Templates Window.

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Templates may also be configured by selecting a template and clicking the Edit Template button.

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Six tabs contain multiple criteria that can be edited and adjusted to meet your specific output needs.

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Once you have your settings selected, click the Draw button to view your Timescaled Logic Diagram.





The output may be printed, saved as an image or saved as a template for use on other projects.



